

FIG. 1

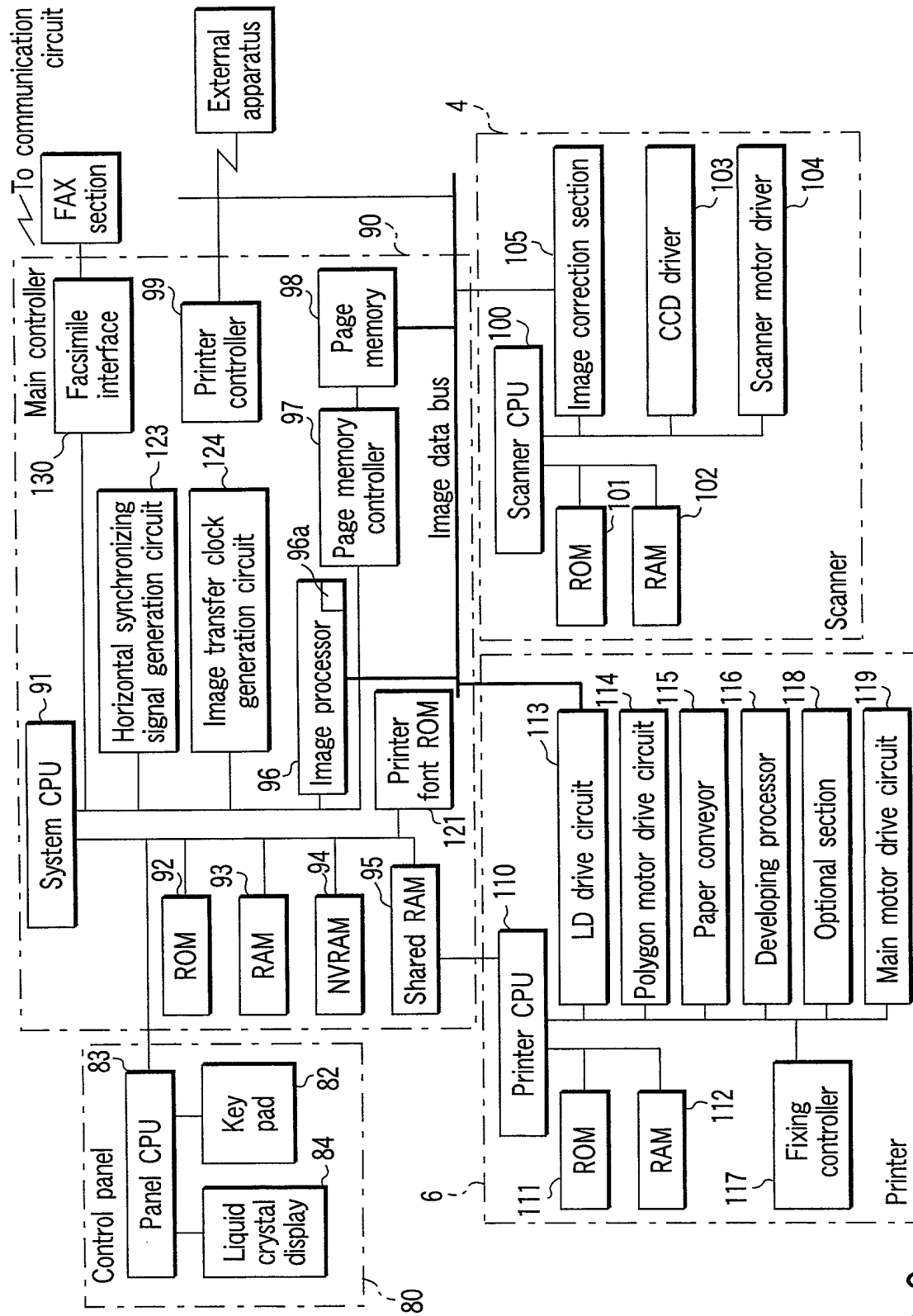


FIG. 2

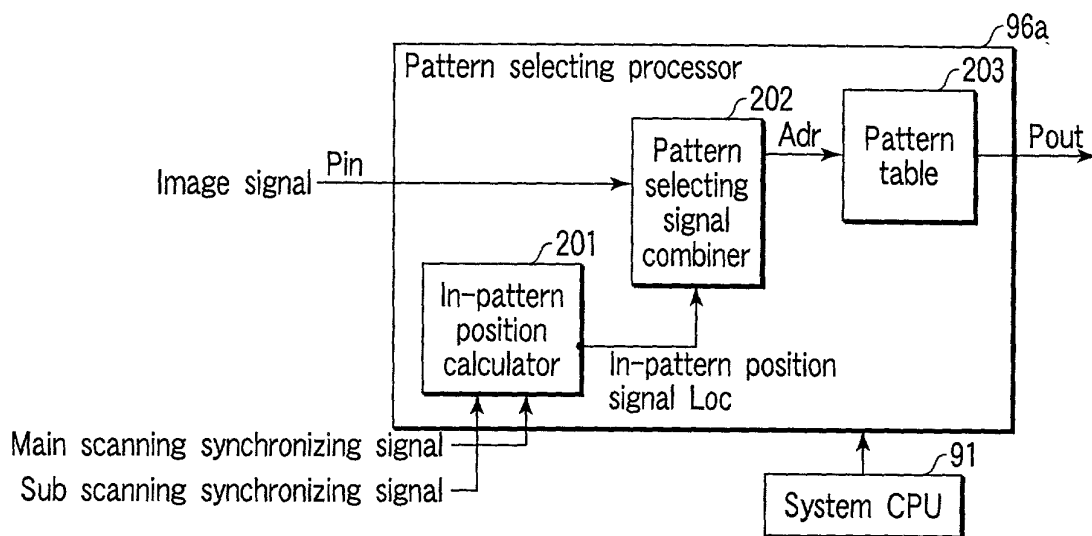


FIG. 3

Loc
 (Represent position in pattern in one dimension)

	00	01	02	03
	04	05	06	07
	08	09	0A	0B
	0C	0D	0E	0F

yd1

xd1

FIG. 4

Pattern corresponding to
 Pin=10 (hexadecimal)

80	00	00	00
00	00	00	00
00	00	00	00
00	00	00	00

FIG. 5A

Pattern corresponding to
 Pin=20 (hexadecimal)

80	00	00	00
00	00	00	00
00	00	80	00
00	00	00	00

FIG. 5B

Pattern corresponding to
 Pin=40 (hexadecimal)

80	00	00	00
00	80	00	00
00	00	80	00
00	00	00	80

FIG. 5C

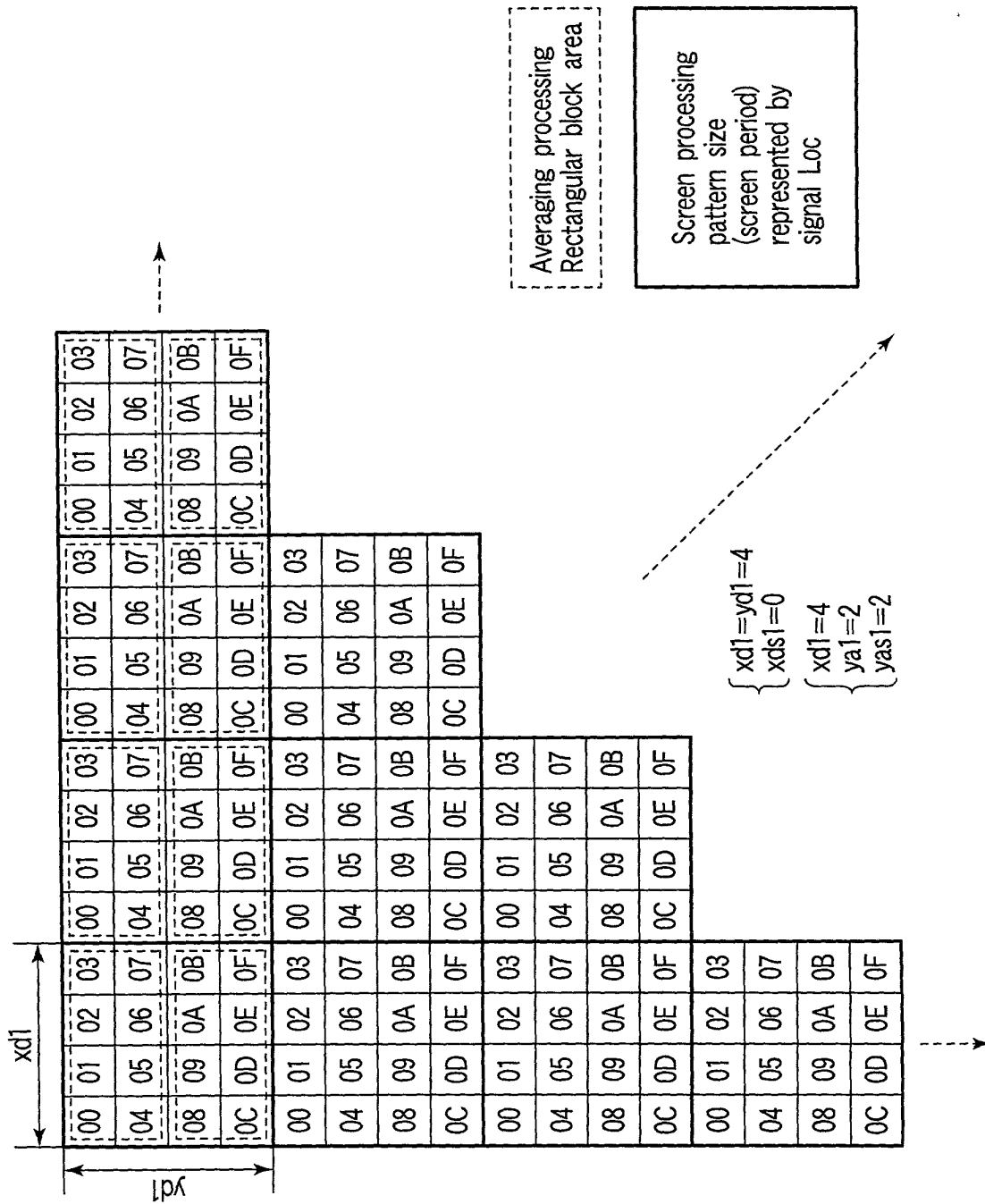
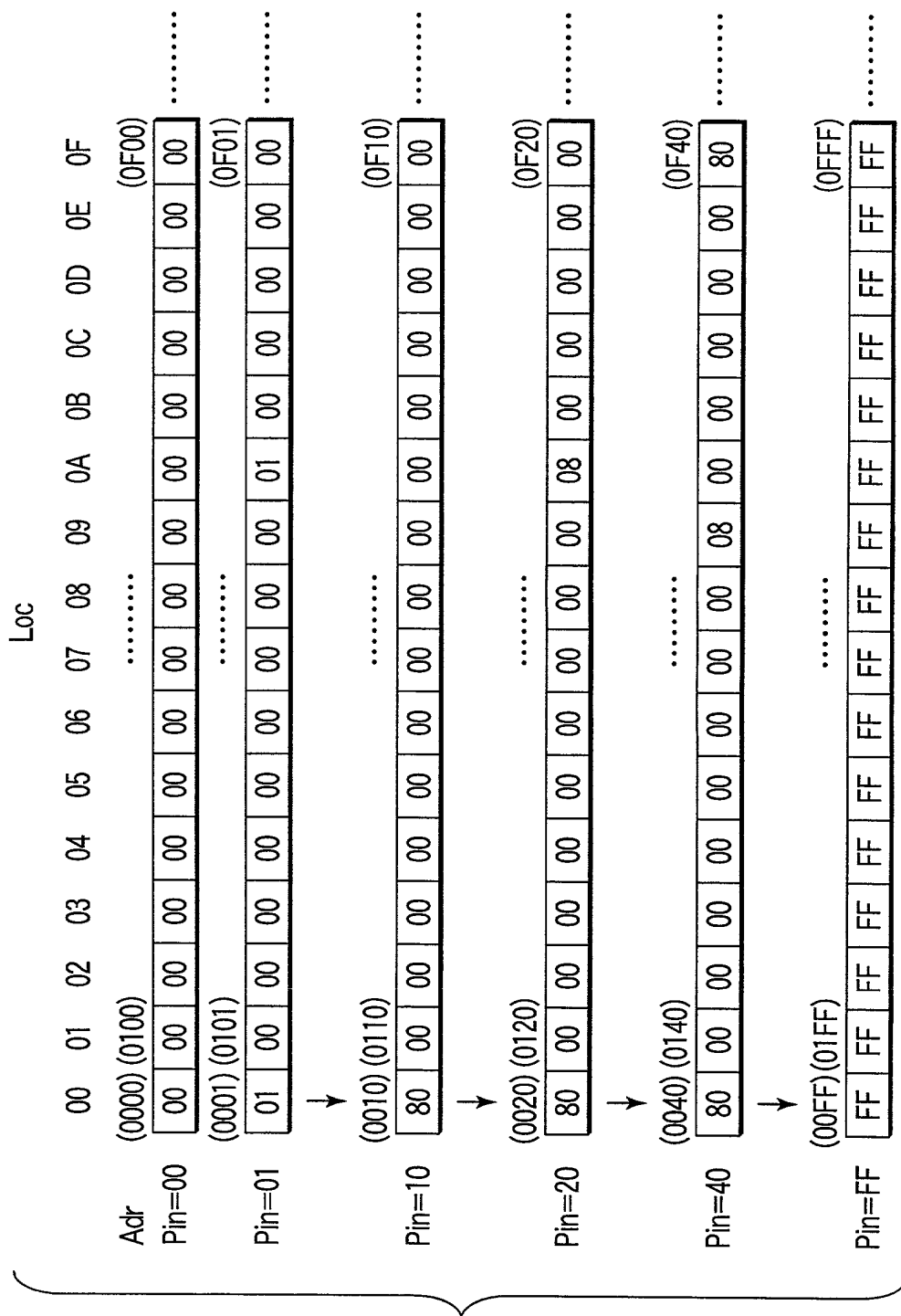


FIG. 6

FIG. 7



TEXT 463F2560

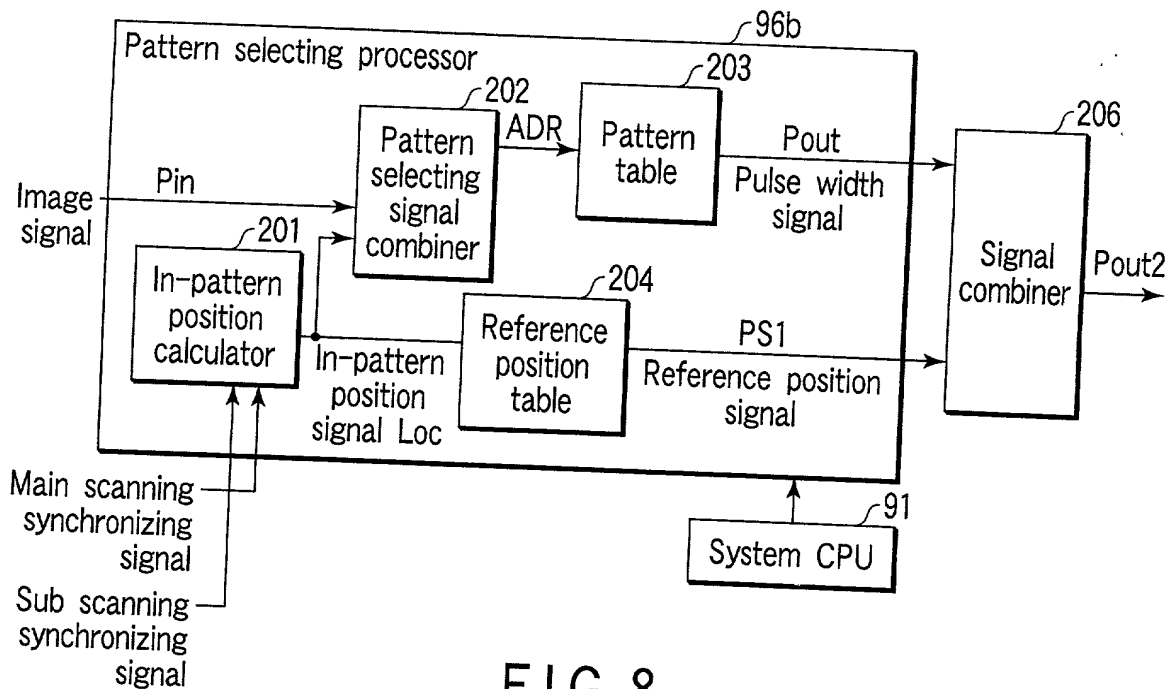


FIG. 8

03	02	00
03	02	00

03 : Right
 02 : Left
 00 : Center

FIG. 9A

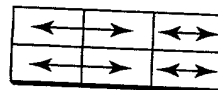


FIG. 9B

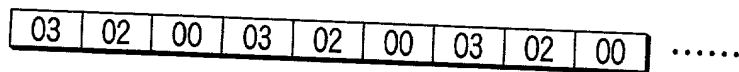


FIG. 9C

80	80	00
00	FF	00

FIG. 9D

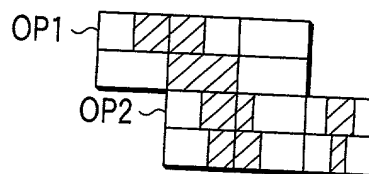


FIG. 9E

FIG. 10

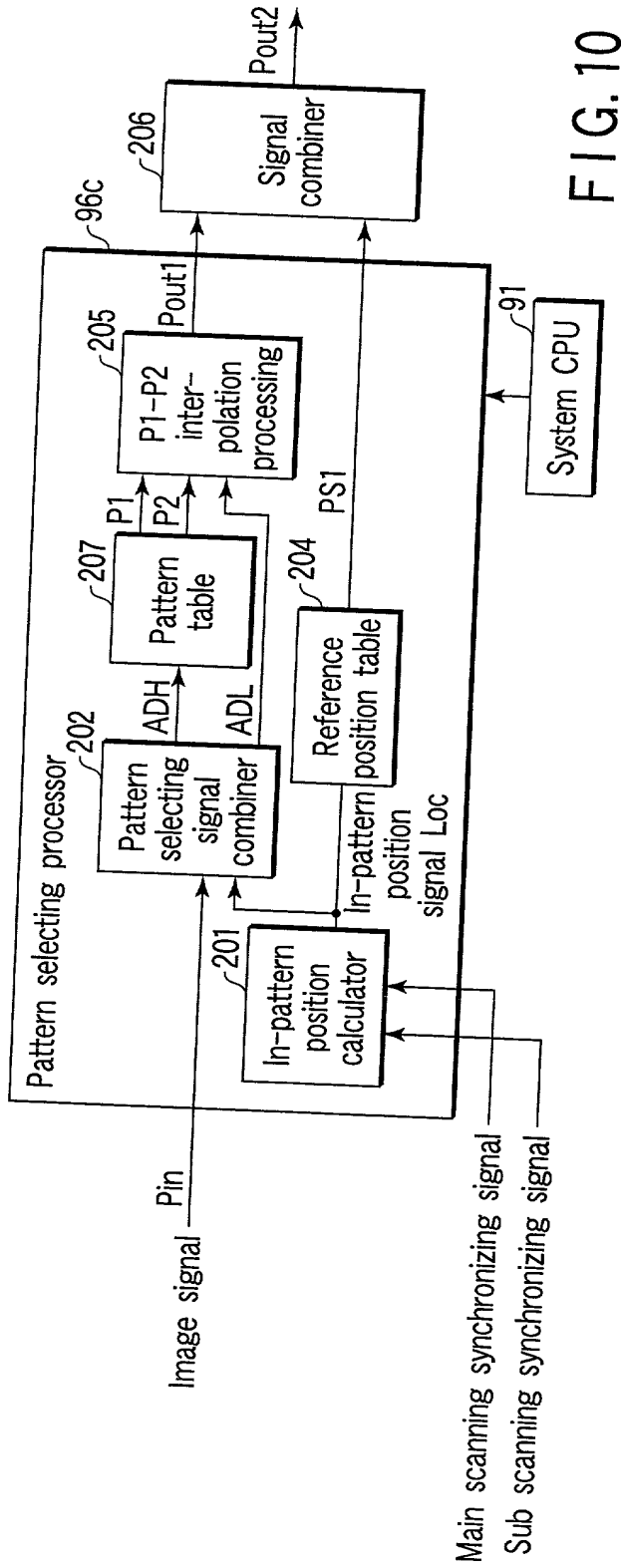
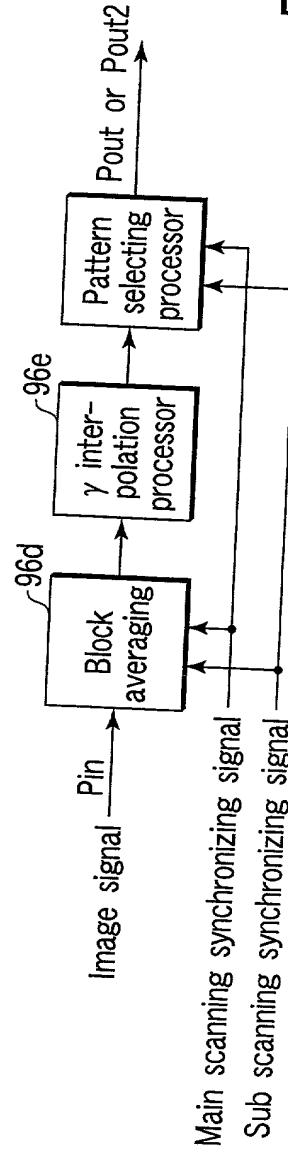


FIG. 10

FIG. 11



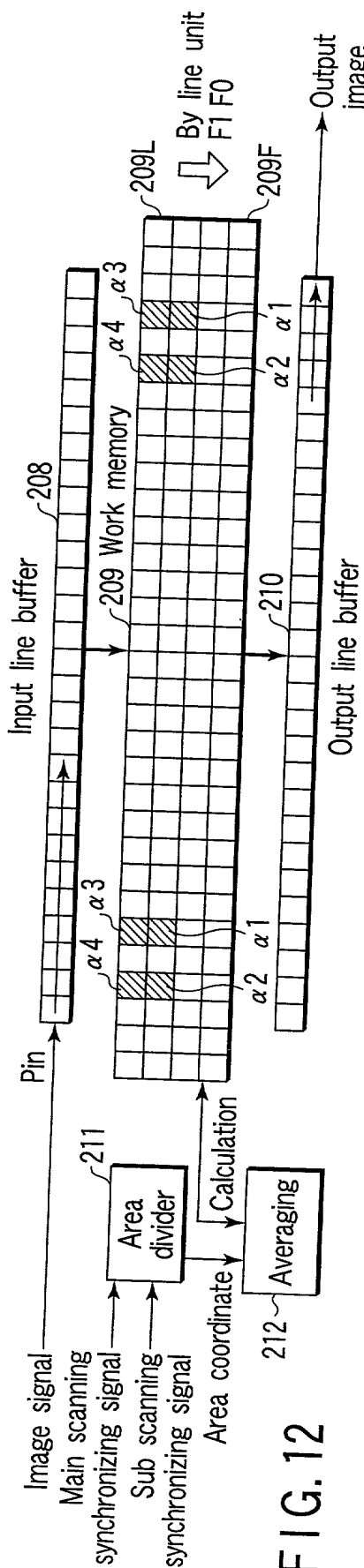


FIG. 12

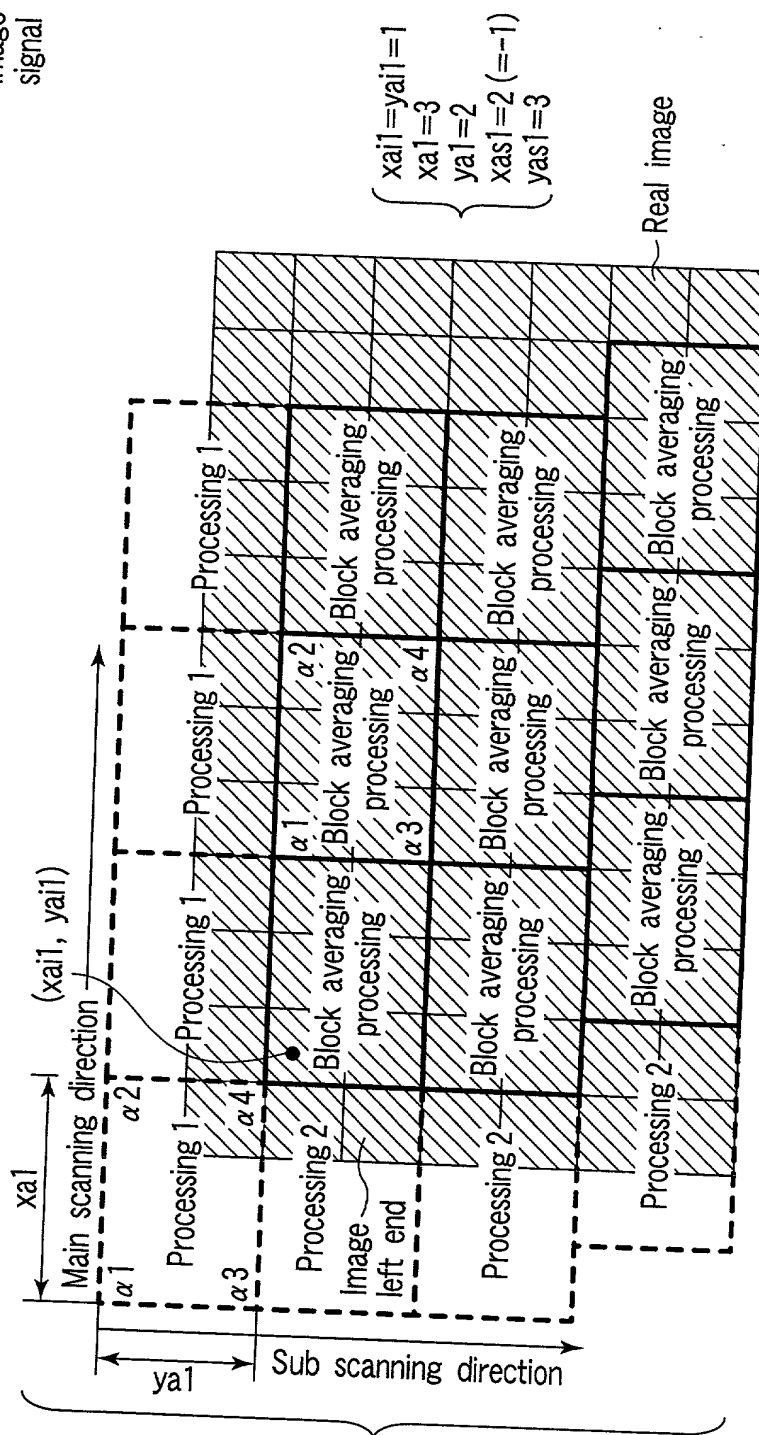


FIG. 13

Patent 4632255

Constitution example of block averaging processing parameter

Symbol	Bit number	Range, code, and the like	Meaning of signal
xai1	3	[0-5], additionally $0 \leq xai1 < xa1$	Main scanning coordinate of processing start of averaging block
yai1	2	[0-2], additionally $0 \leq yai1 < ya1$	Sub scanning coordinate of processing start of averaging block
xa1	3	[1-6]	Size (main scanning) of averaging block
ya1	2	[1-3]	Size (sub scanning) of averaging block
xas1	3	[0-5], additionally $0 \leq xas1 < xa1$	Skew of averaging block
yas1	3	[1-4]	Skew of averaging block

FIG. 14

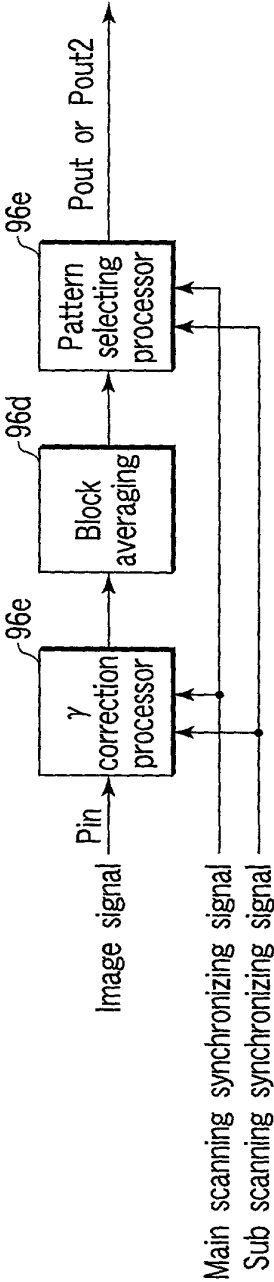


FIG. 15

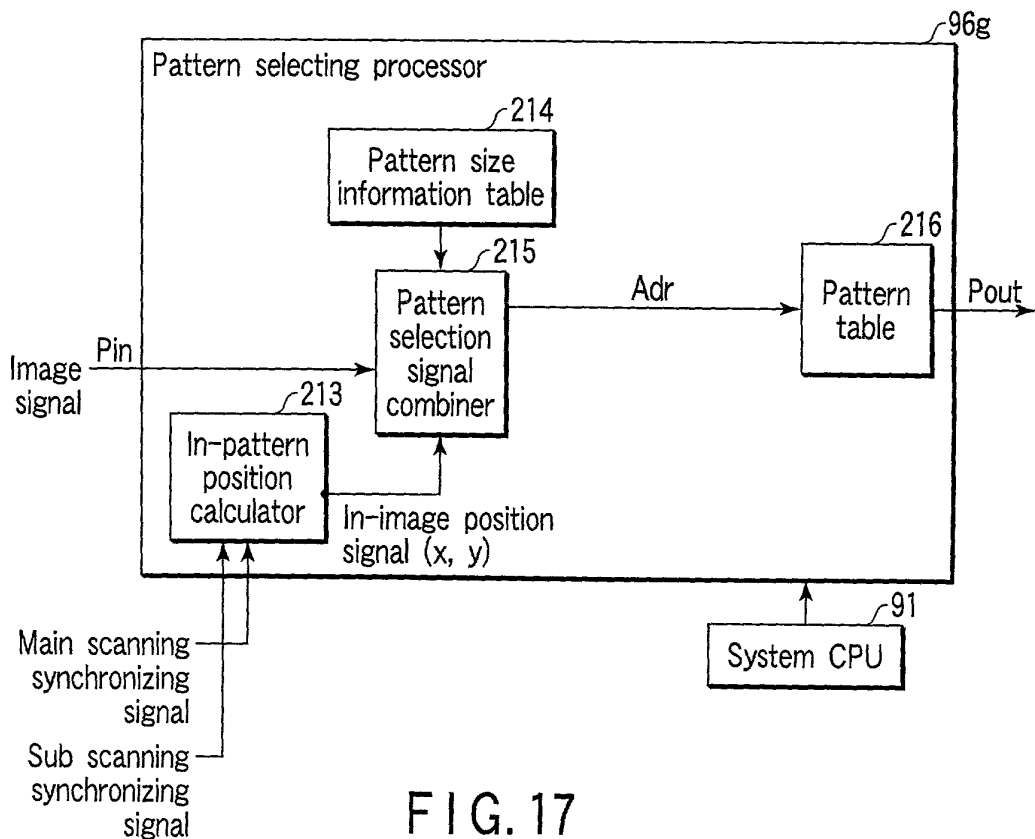
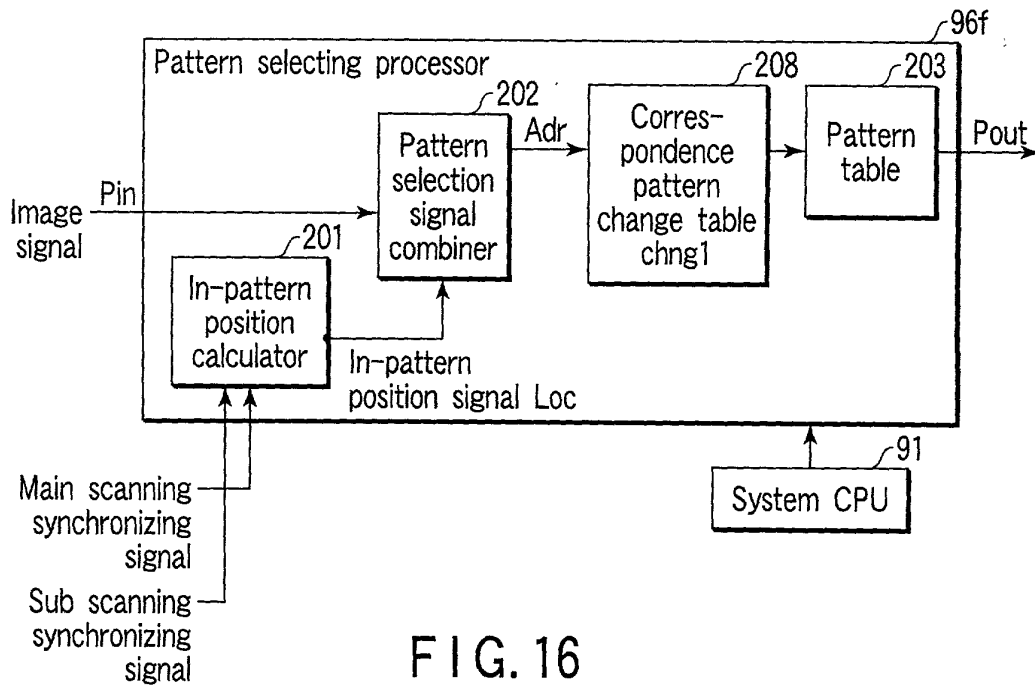


FIG. 18

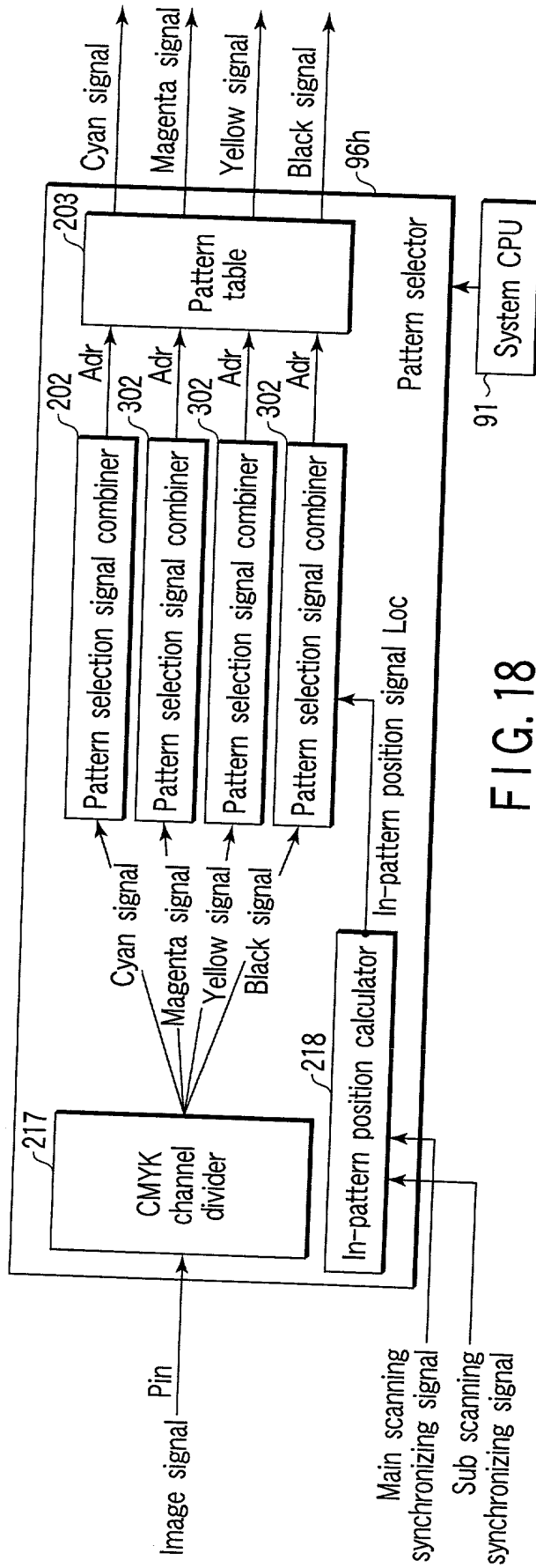


FIG. 18

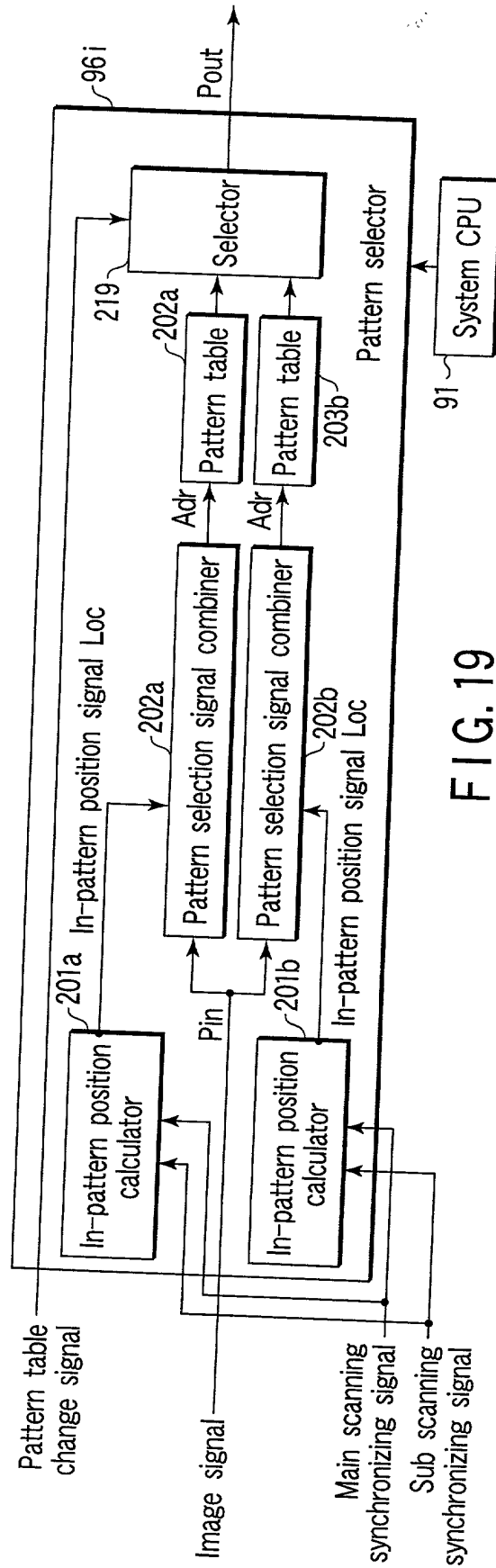


FIG. 19